ATES PATENT AND TRADEMARK OFFICE
Confirmation No.: 8888

Unit: 2818 IN THE

In re PATENT APPLICATION

JEONG, Jae Goan

Appln. No.: 09/751,939

Filed: January 2, 2001

Title: TRANSISTOR AND METHOD FOR FABRICATING THE SAME

April 15, 2002

AMENDMENT

Hon. Commissioner of Patents Washington, D.C. 20231

Sir:

In response to the Office Action dated January 15, 2002, please amend the above identified application as follows:

IN THE CLAIMS:

Please enter the following amended claims:

(Amended) A transistor comprising:

a device isolation film formed on a semiconductor substrate, the device isolation film having a groove therein that exposes a port on of the semiconductor substrate defining an active region and having substantially vertical profile with respect to the exposed portion of the semiconductor substrate;

a gate electrode structure formed in the central portion of the active region, separated from the device isolation film, wherein the gate electrode structure comprises:

a stacked structure $d\mathbf{f}$ a gate oxide film, a first gate electrode and a second gate electrode being formed on the semiconductor substrate;

an oxide layer formed on a side wall of the first gate electrode; and

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Inventor(s): JEONG, Jae Goan	40.020				P 275428	000-OPH-20	55
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REPLY/AMENDMEN	HLENTER			Date:	April 15, 2002	28	
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FEE REQUIREMENTS FOR CLAIMS AS AMENDED							
1. Small Entity claim				Present Extra	Large/Small Entity	Additional	Fee Code
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time (leave blank if this is a reissue application)							
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7. Enter any previous extension fee paid since above original due date and subtract - \$0							
Extension 1 00 1.1.						+ \$0	149/249
8. 9. If Terminal Disclaimer attached, add Rule 20(d) official fee						+ \$0	148/248
40 KIDC attached requires Official Fee under Rule 9/ (C)						+ \$0	126 126
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10. No. of additional inventions for examination per Rule 129(D)						+ \$0	1179/1279
12. No. of additional inventions for examination (RCE) + \$740/3						+ \$0	1179/1277
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